Attorney's Docket No.: 10559-075002 Client's Ref. No.: P7567C

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Applicant : Matthew J. Adiletta

Art Unit: 2183

Fixaminer: Richard L. Ellis

Serial No.: 10/615,500 Filed : July 8, 2003

Assignee : Intel Corporation

Title : PARALLEL PROCESSOR ARCHITECTURE

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

An INFORMATION DISCLOSURE STATEMENT dated August 25, 2006 is attached.

Respectfully submitted,

Date: August 25, 2006

Scott C. Harris Reg. No. 32,030

Attorney for Intel Corporation

Fish & Richardson P.C. USPTO Customer No. 20985 12390 El Camino Real San Diego, CA 92130

Telephone: (858) 678-5070 Facsimile: (858) 678-5099

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BY JOHN F. CONROY REG. NO. 45,485

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Attorney's Docket No.: 10559-075002 / P7567C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

The documents are in the English Language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

CERTIFICATE OF TRANSMISSION BY PACSIMILE

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I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

August 25, 2006

Date of Transmission

Signature

Jennifer H. Payne

Typed or Printed Name of Person Signing Certificate

Attorney's Docket No.: 10559-075002 / P7567C

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). Please apply the rule 17(p) certification fee in the amount of \$180 to Deposit Account No. 06-1050.

Respectfully submitted,

Date: August 25, 2006

Reg. No. 32,030

Attorney for Intel Corporation

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